

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 42390P2445C3	Application Number: Not Yet Assigned
Title: A METHOD AND APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA		First Named Inventor: Alexander D. Peleg	
		Filing Date: Herewith	Page 1 of 4

JC828 U.S. PTO  
09/989736  
11/19/01

U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (If known)			
<i>hu</i>		6,035,316		Peleg, et al.	3/7/2000	
		5,983,256		Peleg, et al.	11/9/1999	
		5,859,997		Peleg, et al.	1/12/1999	
		5,793,661		Dulong, et al.	8/11/1998	
		5,721,892		Peleg, et al.	2/24/1998	
		5,576,983		Shiokawa	11/19/1996	
		5,528,529		Seal	6/18/1996	
		5,517,438		Dao-Trong, et al.	5/14/1996	
		5,509,129		Guttag, et al.	4/16/1996	
		5,506,865		Weaver, Jr.	4/9/1996	
		5,487,022		Simpson, et al.	1/23/1996	
		5,457,805		Nakamura	10/10/1995	
		5,442,799		Murakami, et al.	8/15/1995	

FOREIGN PATENT DOCUMENTS								
Exam. Initial*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (If known)				

Examiner Signature	<i>hu</i>	Date Considered 4/2000
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<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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		Number	Kind Code <sup>2</sup> (If known)			
<i>du</i>		5,420,815		Nix, et al.	5/30/1995	
		5,325,320		Chiu	6/28/1994	
		5,321,644		Schibinger	6/14/1994	
		5,293,558		Narita, et al.	3/8/1994	
		5,262,976		Young, et al.	11/16/1993	
		5,241,492		Girardeau, Jr.	8/31/1993	
		5,227,994		Ohki Mitsuharu	7/13/1993	
		5,222,037		Taniguchi	6/22/1993	
		5,187,679		Vassiliadis, et al.	2/16/1993	
		5,111,422		Ullrich	5/5/1992	
		5,095,457		Jeong	3/10/1992	
		4,989,168		Kuroda, et al.	1/29/1991	
		4,771,379		Ando, et al.	9/13/1988	

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<i>du</i>		4,707,800		Montrone, et al.	11/17/1987	
		4,498,177		Larson	2/5/1985	
		4,418,383		Doyle, et al.	11/29/1983	
		4,393,468		New	7/12/1983	
		4,344,151		White	8/10/1982	
		4,161,784		Cushing, et al.	7/17/1979	
		3,723,715		Chen, et al.	3/27/1973	
		3,711,692		Batcher	1/16/1973	

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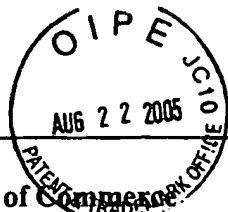
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<b>OTHER ART - NO PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation <sup>2</sup>
A                       V		International Search Report for PCT/US96/12799, Dated 11-26-1996 (1 page)	
		J. SHIPNES, <u>Graphics Processing with the 88110 RISC Microprocessor</u> , IEEE (1992), pp 169-174.	
		<u>MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1991)	
		<u>Errata to MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1992) pp 1-11	
		<u>MC88110 Programmer's Reference Guide</u> , Motorola Inc. (1992), pp 1-4.	
		<u>i860™ Microprocessor Family Programmer's Reference Manual</u> , Intel Corporation (1992), Ch. 1, 3, 8, 12.	
		R.B. LEE, <u>Accelerating Multimedia With Enhanced Microprocessors</u> , IEEE Micro (April 1995), pp 22-32.	
		<u>TMS320C2X User's Guide</u> , Texas Instruments (1993) pp3-2 through 3-11; 3-28 through 3-34; 4-1 through 4-22; 4-41; 4-103; 4-119 through 4-120; 4-150 through 4-151.	
		L. GWENNAP, <u>New PA-RISC Processor Decodes MPEG Video</u> , Microprocessor Report (Jan. 1994), pp 16, 17.	
		SPARC Technology Business, <u>Ultra SPARC Multimedia Capabilities On-Chip Support for Real-Time Video and Advanced Graphics</u> , Sun Microsystems (Sept. 1994)	
		Y. KAWAKAMI et al., <u>LSI Applications: A Single-Chip Digital Signal Processor for Voiceband Applications</u> , Solid State Circuits Conference, Digest of Technical Papers; IEEE International (1980)	
		B. CASE, <u>Philips Hopes to Displace DSPs with VLIW</u> , Microprocessor Report (Dec. 1994), pp 12-18.	
	N. MARGULIS, <u>i860™ Microprocessor Architecture</u> , McGraw Hill, Inc. (1990), Ch. 6, 7, 8, 10, 11.		
	<u>Pentium Processor User's Manual, Volume 3: Architecture and Programming Manual</u> , Intel Corporation (1993), Ch. 1, 3, 4, 6, 8, and 18.		
Examiner Signature	Date Considered		9/20/95

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<b>US Department of Commerce</b> <b>Patent and Trademark Office</b>  <b>Form PTO-1449 (Modified)</b>	<b>Atty. Docket No.:</b> 42390P2445C3	<b>Application No.:</b> 09/989,736
	<b>Applicant:</b> Alexander D. Peleg	
	<b>Filing Date:</b> November 19, 2001	

### US Patent Documents

Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
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	08/19/75	3,900,724	McIver			
	03/02/76	3,941,990	Rabasse et al.			
	09/17/85	4,542,476	Nagafuji, et al.			
	06/30/87	4,677,582	Nagafuji			
	07/26/88	4,760,545	Inagami et al.			
	03/07/89	4,811,269	Hirose et al.			
	12/26/89	4,890,218	Bram			
	07/31/90	4,945,479	Rusterholz et al.			
	11/20/90	4,972,362	Elkind et al.			
	01/15/91	4,985,848	Pfeiffer et al.			
	03/19/91	5,001,662	Baum			
	09/10/91	5,047,975	Patti et al.			
	01/14/92	5,081,698	Kohn			
	06/30/92	5,126,964	Zurawski			
	11/30/92	5,161,247	Murakami et al.			
	02/23/93	5,189,636	Patti et al.			
	05/11/93	5,210,711	Rossmere, et al.			
	08/31/93	5,241,493	Chu et al.			
	04/12/94	5,303,355	Gergen et al.			
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	07/05/94	5,327,369	Ashkenazi			

### Foreign Patent Documents

Examiner's Initials	Date	Document Number	Country	Class	Sub- Class	Translation

### Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

<b>Examiner</b> <div style="border-left: 1px solid black; border-right: 1px solid black; height: 100px; position: relative;"> <div style="position: absolute; top: 0; left: 0; right: 0; height: 20px;"></div> <div style="position: absolute; bottom: 0; left: 0; right: 0; height: 20px;"></div> </div>	<b>Date Considered</b> 9/2005
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Dea		02/14/95	5,390,135	Lee et al.			
		06/20/95	5,426,598	Hagihara			
		05/07/96	5,515,520	Hatta et al.			
		11/26/96	5,579,253	Lee et al.			
		12/31/96	5,590,365	Ide et al.			
		02/25/97	5,606,677	Balmer et al.			
		06/24/97	5,642,306	Mennemeir			
		09/30/97	5,673,427	Brown et al.			
		12/23/97	5,701,508	Glew et al.			
		04/21/98	5,742,840	Hansen et al.			
		06/09/98	5,765,037	Morrison et al.			
		07/07/98	5,778,419	Hansen et al.			
		08/11/98	5,793,661	Dulong, et al.			
		08/11/98	5,794,060	Hansen et al.			
		08/11/98	5,794,061	Hansen et al.			
		09/01/98	5,802,336	Peleg et al.			
		09/08/98	5,806,049	Petruzzi			
<b>Foreign Patent Documents</b>							
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✓		09/15/98	5,809,321	Hansen et al.			
		10/06/98	5,819,101	Peleg et al.			
		10/13/98	5,822,603	Hansen et al.			
		11/10/98	5,835,392	Dulong et al.			
		12/22/98	5,852,726	Lin et al.			
		02/09/99	5,870,619	Wilkinson et al.			
		03/09/99	5,880,983	Elliot et al.			
		03/16/99	5,884,071	Kosaraju			
		03/16/99	5,883,824	Lee et al.			
		08/10/99	5,936,872	Fischer, et al.			
		09/14/99	5,953,241	Hansen et al.			
		11/09/99	5,983,257	Dulong et al.			
		12/21/99	6,006,318	Hansen et al.			
		01/25/00	6,018,351	Mennemeir, et al.			
		03/07/00	6,035,387	Hsu et al.			
		05/21/00	6,041,403	Parker, et al.			
		09/19/00	6,122,725	Roussel et al.			
		11/28/00	6,154,831	Thayer et al.			
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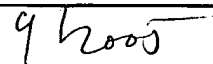
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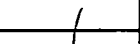

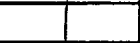
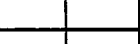
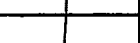
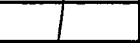
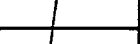
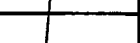


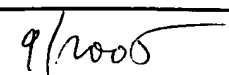
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New		01/23/01	6,178,482	Sollars		
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		05/08/01	6,230,257	Roussel et al.		
		05/15/01	6,233,671	Abdallah et al.		
		07/10/01	6,260,137	Fleck, et al.		
		09/25/01	6,295,599	Hansen et al.		
		04/23/02	6,378,060	Hansen et al.		
		05/07/02	6,385,634	Peleg, et al.		
		07/04/02	3,675,001	Singh		
		10/22/02	6,470,370	Fischer, et al.		
		12/31/02	6,502,117	Golliver et al.		
		06/24/03	6,584,482	Hansen et al.		
		11/04/03	6,643,765	Hansen et al.		
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	7/3/1997	WO 97/23821	PCT			
	12/01/00	GB 2339040	Great Britian			
<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>						
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<b>Foreign Patent Documents</b>						
<b>Examiner's Initials</b>	<b>Date</b>	<b>Document Number</b>	<b>Country</b>	<b>Class</b>	<b>Sub- Class</b>	<b>Translation</b>
<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>						
		Samsung Electronics, "21164 Alpha Microprocessor Data Sheet", 1997.				
		Silicon Graphics, "Silicon Graphics Introduces Enhanced MIPS Architecture to Lead the Interactive Digital Revolution", 10/21/96, 2 pages.				
		Philips Electronics, "TriMedia TM1000 Preliminary Data Book", 1997.				
		"MIPS Digital Media Extension", pp. C1 to C40.				
		"MIPS Extension for Digital Media with 3D", MIPS Technologies, Inc. March 12, 1997, pp. 1-26.				
		"The VIS™ Instruction Set", Sun Microsystems, Inc. 1997, pp. 1-2.				
		Abbott et al., "Broadband Algorithms with the MicroUnity Mediaprocessor", Proceedings of COMPCON '96, 1996, pages 349-354				
		Hayes, et al., "MicroUnity Software Development Enviroment", Proceedings of COMPCOM '96, 1996, pages 341-348.				
		"Silicon Graphics Introduces Compact MIPS@ RISC Microprocessor Code for High Performance at a Low Cost", Oct. 21, 1996, downloaded from the Website webmaster@www.sgi.com, pp. 1-2				
		Killian, Earl, "MIPS Extension for Digital Media", Silicon Graphics, pp. 1-10				
✓		"MIPS V Instruction Set", pp. B1 to B7.				
<b>Examiner</b>				<b>Date Considered</b>		


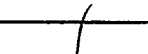
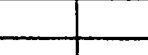
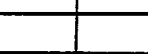

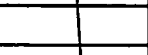

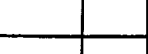

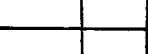
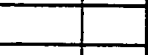


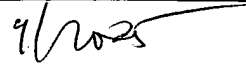
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<b>US Department of Commerce Patent and Trademark Office</b>  <b>Form PTO-1449 (Modified)</b>		<b>Atty. Docket No.:</b> 42390P2445C3		<b>Application No.:</b> 09/989,736		
		<b>Applicant:</b> Alexander D. Peleg				
		<b>Filing Date:</b> November 19, 2001				
<b>US Patent Documents</b>						
<b>Examiner's Initials</b>	<b>Date</b>	<b>Document Number</b>	<b>Name</b>	<b>Class</b>	<b>Sub- Class</b>	<b>Filing Date</b>
<b>Foreign Patent Documents</b>						
<b>Examiner's Initials</b>	<b>Date</b>	<b>Document Number</b>	<b>Country</b>	<b>Class</b>	<b>Sub- Class</b>	<b>Translation</b>
<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>						
		Santoro, Mark; Design and Clocking of VLSI Multipliers. Technical Report No. CSL -TR-89-397, October 1989. Pages i-xii and 1-118.				
		Santoro, Mark, et al.; SPIM: A Pipelined 64X64 bit Interactive Multiplier, IEEE Journal of Solid -State Circuits, Vol. 24, No. 2, April 1989. Pages 487-493.				
		Santoro, Mark, et al.; SESSION II: HIGH-SPEED MICROPROCESSOR. WAM 2.6: A Pipeleined 64X64b Interactive Array Multiplier. 1988 IEEE International Solid State Circuits Conference . Pages 36-37 and 290.				
		BIT Preliminary, Bipolar Integrated Technology, Inc. B3110/B3120;B2110/B2120 Floating Point Chip Set. Pages 1-40.				
		Eklind, Bob, et al. A SUB 10 nS Bipolar 64 Bit Integrated/Floating Point Processor Implemented On Two Circuits. IEEE 1987 BCTM, pages 101- 104				
		Leibowitz, Bob; et al., System Design; ECL gains ground in battle against CMOS. Computer Design; April 1, 1987. Pages 91-95.				
		IBM. TBD: Double Speed, Single Precision Vector Register Organization Using Double Port Chips. Feb. 1981. Pp.1-6.				
		Farmwald, P. Michael; High Bandwidth Evaluation of Elementary Functions, S-1 Project. IEEE 1981. Pages 139 - 142.				
		Farmwald, P. Michael; On the Design of High Performance Digital Arithmetic Units. UCRL-53190. August 1981. Pages i-vii and 1-95.				
		Grimes et al.; 64-Bit Processor. The Intel i860 64-Bit Processor: A General-Purpose CPU with 3D Graphics Capabilities. July 1989. Pages 85-94.				
<b>Examiner</b>				<b>Date Considered</b>		

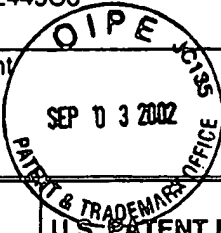
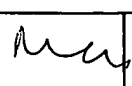
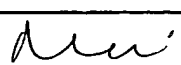
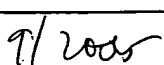
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<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
• <i>ven</i>			Ide, et al.; A 320-MFLOPS CMOS Floating-Point Processing Unit for Superscalar Processors. IEEE Journal of Solid State Circuits, Vol. 28, No. 3, March 1993. Pages 352 - 361.				
			Lino, et al.; ISSCC 92 SESSION 6/ MICROPROCESSORS/ TA 6.5: A 289MFLOPS Single-Chip Supercomputer. 1992 IEEE International Solid State Circuits Conference. Pages 112-113.				
			Kohn, et al.; Introducing the Intel i860 64 Bit Microprocessor, Intel Corp. August 1989. Pages 15 - 30.				
			Lee, Ruby B.; Accelerating Multimedia with Enhanced Microprocessors. Hewlett-Packard. IEEE Micro, April 1995. Pages 22-32.				
			Lee, Ruby B.; Realtime MPEG Video via Software Decompression on a PA-RISC Processor. 1995 IEEE, pages 186-192.				
			Manferdelli, John L. et al.; Signal Processing Aspects of the S-1 Multiprocessor Project. UCRL-84658. July 28, 1980. Pages 1-8.				
			Spaderna, D., et al.; An Integrated Floating Point Vector Processor for DSP and Scientific Computing". SHARP Microelectronics Technology Inc. IEEE 1989. Pages 8-13.				
			Undy, Steve, et al.; A Low-Cost Graphics and Multimedia Workstation Chip Set. April 1994, IEEE micro, pages 10 - 22.				
<b>Examiner</b>	<i>[Signature]</i>			<b>Date Considered</b>			<i>[Signature]</i>

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<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>						
		"64-Bit and Multimedia Extensions In the PA-RISC 2.0 Architecture", Hewlett Packard, downloaded from the Website rblee@cup.hp.com.huck@cup.hp.com, pp. 1-8.				
		"ULTRASPARCHM The Visual Instruction Set (VISM): On Chip Support for New-Media Processing", Sun Microsystems, Inc. 1996, pp. 1-7.				
		"ULTRASPARCHM and New Media Support Real-Time MPEG2 Decode with the Visual Instruction Set (VISM), Sun Microsystems, 1996, pp. 1-8.				
		ULTRASPARCHM Ultra Port Architecture (UPA): The New-Media System Architecture, Sun Microsystems, Inc. 1996, pp.1-4.				
		ULTRASPARCHM Turbocharges Network Operations on New Media Computing, Sun Microsystem, Inc., 1996, pp. 1-5.				
		"The ULTRAPARC Processor - Technology White Paper, Sun Microsystems, Inc. 1995, 37 pages.				
		"AMD-3D <sup>TM</sup> Technology Manual, Advanced Micro Devices, February 1998.				
		Hansen, Craig, "Architecture of a Broadband Mediaprocessor, MicroUnity Systems Engineering, Inc., 1996, pp. 334-354.				
		Levinthal, Adam, et al. "Parallel Computers for Graphics Applications, Pixar, San Rafael, CA, 1987, pp. 193-198.				
		Levinthal, Adam; Porter, Thomas, "Chap-A SIMD Graphics Processor", Computer Graphics Project, Lucasfilm Ltd., 1984, pp. 77-82.				
		Wang, Mangaser, Shrinivan, "A procesor Architecture for 3D Graphics Calculations, Computer Motion, Inc., pp. 1-23.				
		Visual Instruction Set (VIS <sup>TM</sup> ), User's Guide, Sun Microsystems, Inc., verison 1.1 March, 1997.				
<b>Examiner</b> 		<b>Date Considered</b> 				

Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw a line through the citation if not in conformance and not considered. Include a copy of this form with the next communication to the applicant

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REFERENCE DESIGNATION		U.S. PATENT DOCUMENTS					
Examiner Initials		Document No.		Class	Sub- Class	Filing date if appropriate	
	AA						
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FOREIGN PATENT DOCUMENTS							
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	AQ						
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	AS						
	AT						
	AU						
OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)							
	AV	Proceedings; 11 <sup>th</sup> Symposium on Computer Arithmetic, June 29-July 2, 1993, Windsor, Ontario; Edited by Earl Swartzlander Jr., Mary Jane Irwin, Graham Jullien, received 4/11/1993 (pgs. 171-178)					
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